Northolt High School
Scheme of Learning

Year group: 11	Term: Summer 2	Unit duration: 7 weeks	Number of lessons: 7	Unit title: Computational Logic and Truth table

Assessment outcome: Construct truth tables for the following logic gates: NOT, AND and OR	Fertile question? How would you Interpret the results of truth tables ?

Key skills/concepts/knowledge that students should cover	Start RAG	End RAG	Literacy. Key vocabulary/subject terminology that students should cover
• Construct truth tables for simple logic circuits • Interpret the results of truth tables • Create, modify and interpret simple logic circuit diagrams • Use Boolean functions • Binary situations • Understand Logic			Construct Modify Simple Interpret Logic Diagrams Truth Circuits Output Input
Stretch. Key skills/concepts/knowledge that students should cover Explain a situation where a Not Gate is used, where an And and OR gates are used.			**Suggested materials teachers could/should use** **TeachICT, Isacc Computing, A Level Computing textbook** **Computer Science Powerpoints/Worksheets**
			Key home learning tasks students should complete Worksheets